## SlimStack Board-to-Board Connector Core Product Matrix for Hardware Interface Standard

Standard							MIPI D-PHY	MIPI M-PHY	USB 3.1 Gen 2 Type-C	USB 3.1 Gen 2	USB 3.1 Gen 1	HDMI 2.0	DisplayPort 1.4	Serial ATA 3.0	PCI Express 3.0
Impedance ( $\Omega$ )							80 to 125	80 to 110	85±9	90±10	90±10	100±15	100±10	100±15	85±15
Rise Time [ picosecond (%) ]								200 (10 to 90)	40 (20 to 80)	40 (20 to 80)	50 (20 to 80)	200 (10 to 90)	130 (20 to 80)	70 (20 to 80)	35 (10 to 90)
Pitch (mm)	Image	Mated Height	Series No.		Maximum Usable Frequency (GHz)	Data Rate									
()		(mm) 0.60	505066	505070	20	40	1	<b>√</b>	✓	<b>,</b>	✓	~	✓	1	~
	*** ***	0.70	505270	<u>505070</u>	20	40	~	~	~	~	~	~	~	~	~
		0.80	505270	505274	20	40	~	~	~	~	~	~	~	~	~
		0.90	<u>505413</u>	<u>505274</u>	20	40	~	~	~	~	~	~	~	~	~
		1.00	<u>505413</u>	<u>505417</u>	20	40	~	~	~	~	~	~	~	~	~
0.40		0.00	E0E 470	E0E 470	00	40	,							,	
		0.00	<u> 202473</u>	<u>000476</u>	20	40	~	~					~	~	
		0.70	503304	<u>3304</u> <u>503308</u>	20	40	1	~	~	~	~	~	~	~	~
			<u>30000 1</u>												
			<u>503548</u>	<u>503552</u>			~	1	~		~	1	~	~	~
		0.75	<u>505004</u>	<u>505006</u>	20	40	~	~				~	~	~	
		0.80	<u>505550</u>	<u>505551</u>	20	40	~	~	~		~	~	~	~	~
		1.50	51338	55909	20	40	1	1				~	1	1	~
	THE REAL PROPERTY OF	1.80	500913	55909	15	30	~	~				~	~		~
		2.00	51338	501745	20	40	~	~				~	~		
		2.30	<u>500913</u>	501745	15	30	~	~				~			

\*This chart was produced from simulation results. The conformance to the standards is based on the impedance requirements in each specification. \*Please note that the connector's impedance value is subject to the customer's board design, and conformance to the standards may vary based on that board design. \*The "Usable Frequency" of the connector is determined by the frequency at which the insertion loss starts to exceed -3dB. The data rate is calculated by doubling the "Usable Frequency" in this chart. \*Return loss and crosstalk are not taken into account in these values but must be considered (in addition to insertion loss) when assessing the connector's performance. \*Data is subject to change without notice. Circuit assignment: GSSGSSG



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Pitch	Image	Mated Series No. Maxi Height Frod		Maximum Usable	Data Rate										
0.50		(mm)	Receptacle	Plug											
		06.1	<u>34722</u>	00000	61	30	~	<b>↓</b>				~	~		
		2.50	<u>54102</u>	<u>53885</u>	15	30		✓ ✓				~			
		3.00	<u>52991</u>	<u>53748</u>	20	40	~	~				~			
		4.00	<u>52991</u>	<u>53916</u>	15	30	~	~				~			
0.635		6.00	<u>52885</u>	<u>55091</u>	10	20									
		7.00	<u>52885</u>	<u>53625</u>	10	20									
		8.00	<u>52885</u>	<u>53647</u>	10	20									
		9.00	<u>52885</u>	<u>53649</u>	10	20									
		10.00	<u>52885</u>	<u>53627</u>	10	20									
		12.00	<u>52901</u>	<u>55091</u>	5	10									
		13.00	<u>52901</u>	<u>53625</u>	5	10									
		14.00	<u>52901</u>	<u>53647</u>	5	10									
		15.00	<u>52901</u>	<u>53649</u>	5	10									
		16.00	<u>52901</u>	<u>53627</u>	5	10									
0.635 Floating		12.00	<u>209168</u>	<u>55091</u>	5	10									
		13.00	<u>209168</u>	<u>53625</u>	5	10									
		14.00	<u>209168</u>	<u>53647</u>	5	10									
		15.00	<u>209168</u>	<u>53649</u>	5	10									
		16.00	<u>209168</u>	<u>53627</u>	5	10									

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## www.molex.com/link/slimstack.html

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Circuit assignment: GSSGSSG

